

## SM2258 L06B F/W Release Note

FW Package	MP Tool Version	ISP Version	Note
Q0710B	Q0718A	Q0710B	<ul style="list-style-type: none"> <li>● Add 3 Channel support</li> <li>● Modify for potential SSC disable issue</li> <li>● Fix LTS result issue in RDT FW</li> </ul>
Q0525A	Q0525A	Q0525A	<ul style="list-style-type: none"> <li>● Fix ID table Word 69</li> <li>● Fix Sanitize issue</li> <li>● Add 0xFD command for DevSLP function</li> <li>● Fix download ucode PIO mode can't return status issue</li> <li>● Modify DST feature flow</li> <li>● Enhance SLC wear-leveling policy</li> </ul>
Q0330C	Q0406B	Q0330C	<ul style="list-style-type: none"> <li>● Support TCG function (enable by MP option). Industrial grade only.</li> <li>● Fix Security Erase flag non-update issue at WPRO meta-data.</li> <li>● Add bypass 1~3 bit ECC Adj. fail on Pretest</li> <li>● Fix EOB not complete issue</li> <li>● Fix false power on count when DevSLP</li> <li>● Modify DRAM VDT voltage</li> <li>● Add thermal throttling function</li> <li>● Fix erase count fall back issue</li> <li>● Record retry pass count and maximum hard decode count in RDT flow</li> </ul>
Q0125A	Q0125A	Q0125A	<ul style="list-style-type: none"> <li>● Fix all block 0 is bad issue.</li> <li>● Fix DevSlp issue at Intel flash.</li> <li>● Fix under 2TB out of range problem</li> <li>● Optimize PowerOnHour count</li> <li>● Fix DevSlp when HIPM and DIPM not enabled</li> <li>● Force Gen2 or Gen1 , set in MP tool</li> <li>● Modify ID table W77[2:0] to fix Ulink SATA compliance fail issue.</li> <li>● FW abort 28bit r/w cmd if LBA = 0xFFFFFFFF.</li> <li>● Fix WPRO issue.</li> </ul>
P1026A	P1026A	P1026A	<ul style="list-style-type: none"> <li>● Modify for cache program performance.</li> <li>● Fix WPRO lost issue</li> <li>● Fix ECC fail buffer flag issue.</li> <li>● Fix SLC erase count issue.</li> <li>● Fix 2 Die DRAM issue.</li> <li>● Add polling state for erase command.</li> <li>● Add DDR300 setting.</li> <li>● Add Setting SSC range from CID option.</li> <li>● Add Erase delay time setting from CID</li> <li>● Add Mira DRAM setting</li> </ul>
P0818A	P0817A	P0818A	<ul style="list-style-type: none"> <li>● Bug fix of 1TB burn-in issue (parameter overflow).</li> <li>● Correct ID table.</li> <li>● Enable 2TB support.</li> </ul>
P0802A	P0729A	P0802A	<ul style="list-style-type: none"> <li>● Bug fix and enhancement of RDT function.</li> <li>● Bug fix of SPOR issue.</li> <li>● Enable forced SLC FW algorithm option (with this feature, L06B LPP is not required).</li> </ul>

P0714B	P0714A	P0714B	● Initial enablement of Intel L06B.
P0615A	P0329A	P0615A	● Initial enablement of Micron L06B.

**Note:**

1. F/W and ISP update is recommended.
2. History # is denoted by “Version-Date”.

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